

LOW PROFILE cPCI BACKPLANES

Rev. C



MECHANICAL SPECIFICATIONS

- 3U - 3 slot
- 6U - 2, 3, 4, 6, 8 slots
- 7U - 2, 3 slots
- 32-bit, 64-bit (32-bit capable)

BOARD SPECIFICATIONS

- 2 oz. copper power and ground
- PCB UL recognized 94V-0
- PCB FR-4 or equivalent
- PCB .134" thick, (.125") 2-slot, (.140") 3-slot, PICMG 2.16 versions vary
- 10-layers, 8-layers (2-slot), PICMG 2.16 versions vary

The Bustronic Low Profile cPCI backplane series saves a slot-size of width on the backplanes. By placing power studs and power blades throughout key locations, as opposed to the side of the backplane, space is saved. This allows the 4-slot Low Profile cPCI to fit within a 2U horizontal chassis and the 8-slot to fit within a 4U horizontal chassis, etc.

All Bustronic cPCI backplanes are designed to be fully compliant to the PICMG basic specification 2.0 R3.0 and Hot Swap specification 2.1 R1.0. The H.110 versions conform to PICMG H.110 Computer Telephony specification 2.5 R1.0. Some configurations are compliant to the PICMG 2.16 Packet Switching Backplane specification.

Bustronic uses stripline construction to assure the highest possible performance. By exclusively utilizing stripline construction, we eliminate a significant source of EMI/RFI radiation and give all signals similar characteristic impedances and minimal signal skew. This allows for significantly higher data transfer rates as signal skew factors into the rate calculations four times.

FEATURES

- Conforms to PICMG basic specification 2.0 R3.0
- PICMG Hot Swap specification 2.1 R1.0
- Versions conforming to PICMG 2.16 are available
- Versions conforming to PICMG 2.5 R 1.0 for Computer Telephony are available
- Designed to save a slot size of width, fit into low profile horizontal chassis
- 8-layer and 10-layer controlled impedance stripline designs
- Virtually zero crosstalk

3U Low Profile

The 3U Low Profile line currently consists of a 3-slot left justified version that is 32-bit. For more information on this backplane or for custom 3U Low Profile designs, contact Bustronic.

6U Low Profile

Various Bustronic 6U cPCI backplanes come in the Low Profile layout. This includes most of the PICMG 2.16 cPSB and PICMG 2.17 StarFabric backplanes, and a couple of 32-bit designs. See the Ordering Information on page 3 for details on each style or contact Bustronic for further information.

7U Low Profile

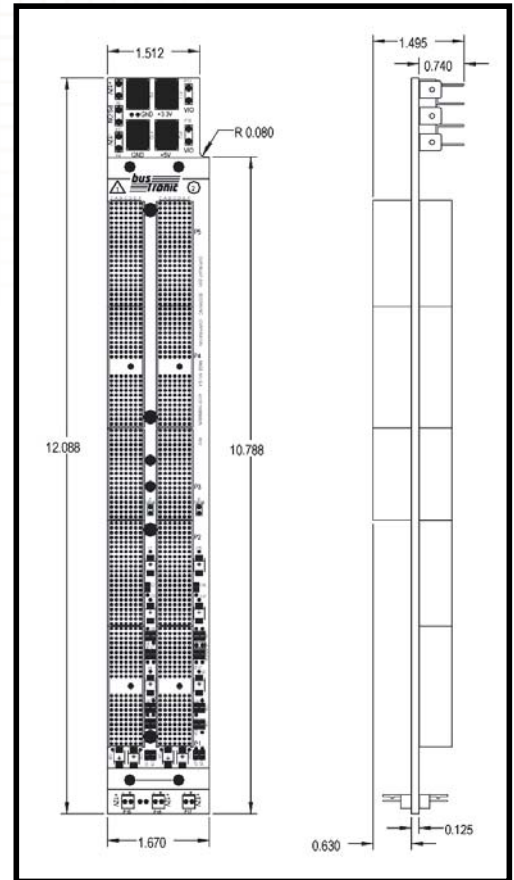
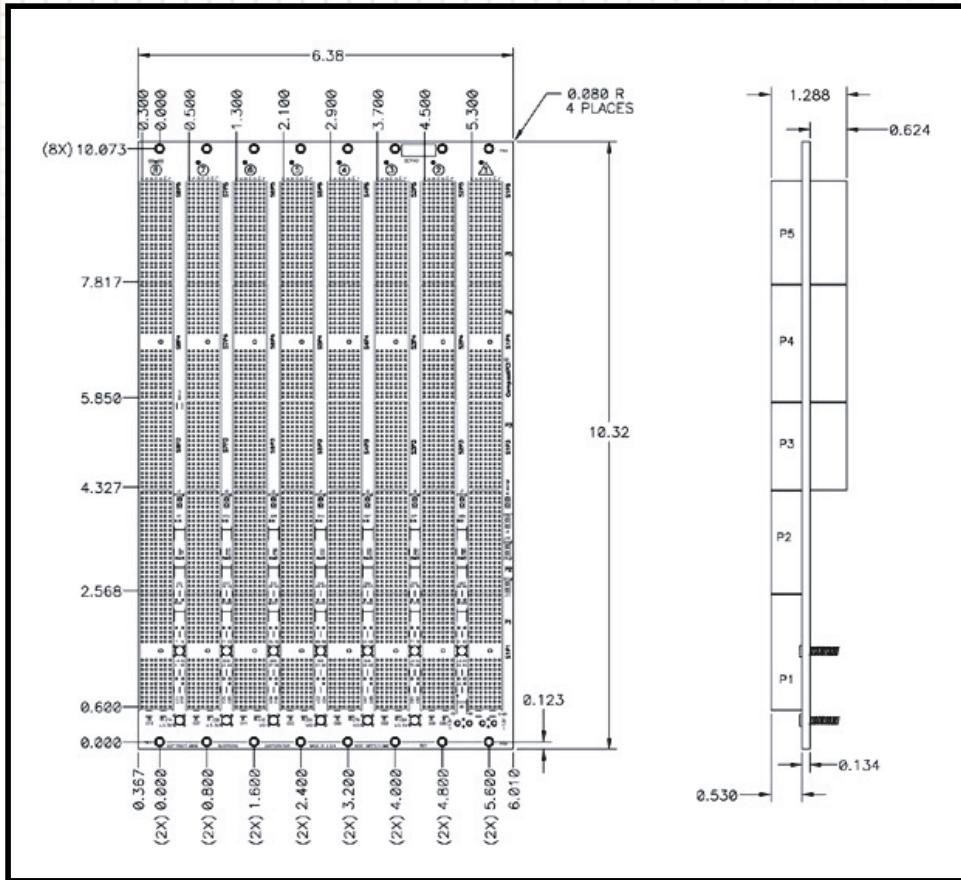
Currently, three versions of Bustronic's Low Profile cPCI backplanes come in 7U length, with the power connections in the "extra 1U" of height. Standard 6U cPCI boards are used with these backplanes. The 2-slot cPCI backplane features include two power bugs for GND, one power bug each for +3.3V and +5V; one power blade each for +12V, -12V, and PS-ON, and two power blades for V I/O. Three +12V fan tray headers are located on the front, with one on the back. The design also includes a backplane stiffener to help prevent the backplane from bowing. Also, a thin profile design next to the power bugs allows cable access from the other side of the chassis.

The 3-slot cPCI backplane features one 20-pin ATX connector, three 3-pin and two 2-pin friction lock headers, a 3-pin V I/O bridge, and one 3-pin fan power connector. Other features include a backplane stiffener and a thin profile design next to the ATX connector. For drawings or a photo of the 3-slot backplane, please contact Bustronic.

Shelf manager connection

Some versions of our Low Profile cPCI backplane contain a 20-pin header. Incorporated into the header with other signal functions are pins for the Intelligent Platform Management Bus (IPMB). These connections are usable for standard shelf managers, or the IPM Sentry.

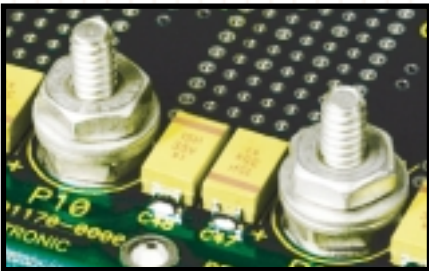
The IPM Sentry is an advanced unit that goes beyond basic monitoring of voltages, fans, and temperature by adjusting the speed of the fans, sending out remote alarms, maintaining a System Event Log (SEL) along with a Sensor Data Record (SDR), collect information on the Field Replaceable Units, and more. Contact Bustronic for more info.



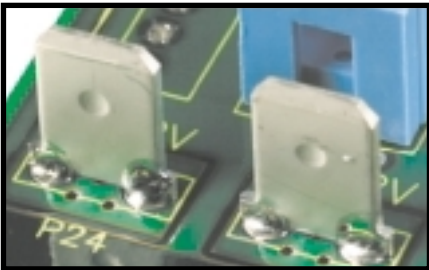
Ordering Information

Height	Slots	Width	Thickness	Description	Order Number
3U	3	2.470	.125	cPCI only, lt. justified, 32-bit	105CP3R303
6U	4	3.180	.134	cPCI only 20-pin header, rt justified	105CPCI604
6U	6	4.708	.134	PICMG 2.16,1 fabric, 5 nodes w/ cPCI, rt. justified	107PS11606
6U	8	6.380	.125	cPCI only, lt. Justified	105CPCI608
6U	8	6.380	.125	cPCI only, lt. Justified, 32-bit	105CP3I608
6U	8	6.380	.125	cPCI and H.110, lt. Justified	105CTEL608
6U	8	6.380	.134	PICMG 2.16, 1 fabric, 7 nodes w/cPCI, rt. justified	107PS11608
6U	8	6.380	.134	PICMG 2.16, 1 fabric, 7 nodes w/cPCI and H.110 , rt. justified	108PS11608
6U	8	6.380	.152	PICMG 2.16, 2 fabric, 6 nodes w/cPCI, 20-pin header, rt. justified	107PS21608
6U	8	6.380	.152	PICMG 2.16, 2 fabric, 6 nodes w/cPCI and H.110, rt. justified	108PS21608
7U	2	1.670	.125	cPCI only, lt. justified	105CPCI702
7U	2	1.670	.125	cPCI and H.110, lt. justified	105CTEL702
7U	3	2.470	.140	cPCI only, ATX, rt. justified	105CPCI703

DESIGN ELEMENTS



Power studs



Fastons

Power Distribution

The Bustronic Low Profile CPCI backplane series is designed with the 6/32 PEM studs distributed between the slots throughout the backplane. Adequate numbers of 6/32 studs and faston blades are available to accommodate more power than the 28 amps required per slot.

Signal Layout

The Bustronic design conforms to the PICMG basic specification 2.0 R3.0 and basic Hot Swap specifications 2.0 R1.0. Some versions comply with the PICMG 2.5 R 1.0 Computer Telephony and/or PICMG 2.16 Packet Switching specifications. A minimum stub length is utilized in routing and interconnecting to the signal traces. Our design techniques avoid crosstalk and noise caused by inadequate ground and power.

Jumpering

Jumpers can be installed to close a circuit. The backplane has labeled areas for jumper installation. The following applies to all of Bustronic's CompactPCI and H.110 backplanes in 2-8 slot sizes. Configurations with 2-5 slots have an additional jumper consideration, the M66EN# jumper.

64-EN# Jumper

If the jumper is installed, 64-EN# P2-B5 (see Hot Swap specification, PICMG 2.1) is ground, and 64-bit boards will initialize for 64-bit operation. If the jumper is not installed, 64-EN# is open, and 64-bit boards will initialize for 32-bit operation.

2-5 Slot Backplanes Only:

M66EN# Jumper

If the jumper is installed M66EN# P1-D21 is ground and the backplane operates in 33MHz mode. If the jumper is not installed M66EN# is bussed and the backplane operates in 66MHz mode.

